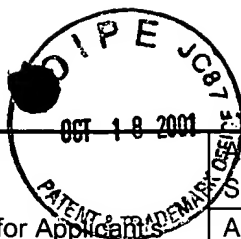


Form PTO-1449 (modified)



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09/933,532

List of Patents and Publications for Applicant's

Applicant
FARID NAJM ET AL.**RECEIVED**

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INFORMATION DISCLOSURE STATEMENT

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Filing Date:

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U.S. Patent Documents

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Foreign Patent Documents

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Other Art

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U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A1						

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
JSP	C1	James W. Nilsson, "Electric Circuits," Second Edition, 1986, pp. 161-167.
JSP	C2	Jan M. Rabaey, "Digital Integrated Circuits-A Design Perspective," Prentice Hall, 1996, pp. 141-146.

Examiner:

Date Considered:

10/24/2004

EXAMINER: initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.